CLAIMS

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	wna	a is claimed is:	
1	1.	A light-emitting panel comprising:	
2	a firs	t substrate, wherein the first substrate comprises a plurality of sockets;	
3	a plurality of micro-components, wherein each micro-component comprises a shell at		
4	least partially filled with a plasma-forming gas and wherein at least one micro-component of the		
5	plurality of micro-components is at least partially disposed in each socket;		
6	a second substrate, wherein the second substrate is opposed to the first substrate such that		
7	the at least one micro-component is sandwiched between the first substrate and the second		
8	substrate; and		
9	a plurality of electrodes, wherein at least two electrodes of the plurality of electrodes are		
10	adhered to only the first substrate, only the second substrate, or at least one electrode is adhered		
11	to each of the first substrate and the second substrate and wherein the at least two electrodes are		
12	arranged so that voltage supplied to the at least two electrodes causes one or more micro-		
13	components	to emit radiation.	
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1	2.	The light-emitting panel of claim 1, wherein the second substrate comprises at	
2	least one len	s.	
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1	3.	The light-emitting panel of claim 2, wherein the at least one lens is configurable	
2	to adjust a field of view of the light-emitting panel.		
1	4.	The light-emitting panel of claim 1, wherein the second substrate comprises at	
2	least one filter.		
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1	5.	The light-emitting panel of claim 4, wherein the plurality of micro-components	

phosphor to convert the ultraviolet radiation to visible light, and wherein the at least one filter changes the visible light passing through the filter to visible light of a specific color. 4

are configured to emit ultraviolet radiation, wherein each micro-component is coated with

1	o. A fight-enutting paner, comprising.	
2	a first substrate, wherein the first substrate comprises a plurality of sockets;	
3	a plurality of micro-components, wherein each micro-component comprises a shell at	
4	least partially filled with a plasma-forming gas and wherein at least one micro-component of the	
5	plurality of micro-components is at least partially disposed in each socket;	
6	a second substrate, wherein the second substrate is opposed to the first substrate such that	
7	the at least one micro-component is sandwiched between the first substrate and the second	
8	substrate; and	
9	a plurality of electrodes, wherein at least two electrodes of the plurality of electrodes are	
10	arranged so that voltage supplied to the at least two electrodes causes one or more micro-	
11	components to emit radiation throughout the field of view of the light-emitting panel without	
12	crossing the at least two electrodes.	
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1	7. The light-emitting panel of claim 6, wherein the first substrate dissipates heat	
2	from the light-emitting panel.	
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1	8. A light-emitting panel comprising:	
2	a first substrate, wherein the first substrate comprises a plurality of sockets and wherein	
3	each socket comprises at least one enhancement material;	
4	a plurality of micro-components, wherein each micro-component comprises a shell at	
5	least partially filled with a plasma-forming gas and wherein at least one micro-component of the	
6	plurality of micro-components is at least partially disposed in each socket;	
7	a second substrate, wherein the second substrate is opposed to the first substrate such that	
8	the at least one micro-component is sandwiched between the first substrate and the second	
9	substrate; and	
10	a plurality of electrodes, wherein at least two electrodes of the plurality of electrodes are	
11	arranged so that voltage supplied to the at least two electrodes causes one or more micro-	
12	components to emit radiation.	

1	9.	The light-emitting display of claim 8, wherein the at least one enhancement	
2	material is di	sposed in or proximate to each socket and wherein the at least one enhancement	
3	material is se	elected from a group consisting of transistors, integrated-circuits, semiconductor	
4	devices, indu	actors, capacitors, resistors, control electronics, drive electronics, diodes, pulse-	
5	forming netv	vorks, pulse compressors, pulse transformers, and tuned-circuits.	
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1	10.	The light-emitting display of claim 9, wherein the at least one enhancement	
2	material self-	-aligns in each socket.	
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1	11.	The light-emitting display of claim 9, further comprising a controller, wherein the	
2	controller se	lectively controls the operation of the at least one enhancement material to adjust at	
3	least one cha	tracteristic of the micro-component.	
		··	
1	12.	A curing or sterilization device comprising a light-emitting panel, wherein the	
2	light-emittin	g panel comprises:	
3	a firs	t substrate, wherein the first substrate comprises a plurality of sockets;	
4	a plu	rality of micro-components, wherein each micro-component comprises a shell at	
5	least partiall	y filled with a plasma-forming gas, wherein the micro-component is configured to	
6	emit ultraviolet light, and wherein at least one micro-component of the plurality of micro-		
7	components is at least partially disposed in each socket;		
8	a sec	ond substrate, wherein the second substrate is opposed to the first substrate such that	
9	the at least o	ne micro-component is sandwiched between the first substrate and the second	
10	substrate; an	d	
11	a plu	rality of electrodes, wherein at least two electrodes of the plurality of electrodes are	
12	arranged so that voltage supplied to the at least two electrodes causes one or more micro-		
13	components	to emit the ultraviolet light.	
1			
1	13.	The curing or sterilization device of claim 12, wherein the at least two electrodes	
2	of the plural	ity of electrodes are adhered to only the first substrate, only the second substrate, or	
3	at least one	electrode is adhered to each of the first substrate and the second substrate.	

1	14.	The curing or sterilization device of claim 12, wherein the ultraviolet light is	
2	emitted throu	ighout the field of view of the light-emitting panel without crossing the at least two	
3	electrodes.		
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1	15.	A high resolution light-emitting panel, comprising:	
2	a firs	substrate, wherein the first substrate comprises a plurality of sockets;	
3	a plu	rality of micro-components, wherein each micro-component comprises a shell at	
4	least partially	y filled with a plasma-forming gas, wherein the micro-component is configured to	
5	emit radiatio	n, and wherein at least one micro-component of the plurality of micro-components is	
6	at least partia	ally disposed in each socket;	
7	a sec	ond substrate, wherein the second substrate is opposed to the first substrate such that	
8	the at least one micro-component is sandwiched between the first substrate and the second		
9	substrate; and		
0	a plu	rality of electrodes, wherein at least two electrodes of the plurality of electrodes are	
11	arranged so that voltage supplied to the at least two electrodes causes one or more micro-		
12	components	to emit radiation.	
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1	16.	A web fabrication process for manufacturing a plurality of light-emitting panels,	
2	the process of	comprising the steps of:	
3	provi	ding a first substrate;	
4	dispo	sing a plurality of micro-components on the first substrate;	
5	dispo	sing a second substrate over the first substrate such that the plurality of micro-	
6	components are sandwiched between the first substrate and the second substrate; and		
7	dicin	g the first substrate and the second substrate to form individual light-emitting panels	
1	17.	The process of claim 16, wherein the first substrate, the second substrate or the	
2	first substrat	e and the second substrate are provided as rolls of material.	
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1	18.	The process of claim 16, wherein the first substrate comprises a plurality of	
2	sockets.		

29 1 19. The process of claim 16, further comprising the step of disposing at least two 2 electrodes on the first substrate, the second substrate, or at least one electrode on each of the first 3 substrate and the second substrate. 1 20. 1 The process of claim 19, wherein each light-emitting panel comprises the at least 2 two electrodes, wherein the at least two electrodes are adhered to only the first substrate, only the 3 second substrate, or at least one electrode is adhered to each of the first substrate and the second 4 substrate, and wherein the at least two electrodes are arranged so that voltage supplied to the at 5 least two electrodes causes one or more micro-components to emit radiation. 1 21. 1 The process of claim 19, wherein each light-emitting panel comprises the at least 2 two electrodes and wherein the two electrodes are arranged so that voltage supplied to the at least 3 two electrodes causes one or more micro-components to emit radiation throughout the field of 4 view of the individual light-emitting panel without crossing the at least two electrodes. 1 1 22. The process of claim 16, further comprising the steps of forming a plurality of 2 sockets in the first substrate and wherein the step of disposing a plurality of micro-components 3 on the first substrate further comprises at least partially disposing at least one micro-component

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23. The process of claim 22, wherein the first substrate comprises a plurality of material layers and wherein the step of forming a plurality of sockets in the first substrate comprises the steps of selectively removing a plurality of portions of the material layers to form a plurality of cavities.

of the plurality of micro-components in each socket of the plurality of sockets.

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24. The process of claim 22, wherein the step of forming a plurality of sockets in the first substrate comprises the steps of patterning the first substrate with a plurality of cavities.

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25. The process of claim 24, further comprising the steps of disposing at least one material layer on the first substrate so that the at least one material layer conforms to the shape of

each socket of the plurality of sockets and disposing at least one electrode between the first substrate and the at least one material layer.

26. The process of claim 24, further comprising the steps of disposing a plurality of material layers on the first substrate so that the plurality of material layers conform to the shape of each socket of the plurality of sockets and disposing at least one electrode within the plurality of material layers.

27. The process of claim 22, wherein the step of providing a first substrate comprises the step of forming a first substrate by disposing a plurality of material layers and wherein the step of forming a plurality of sockets in the first substrate further comprises the step of selectively removing a plurality of portions of the material layers to form a plurality of cavities.

28. The process of claim 27, further comprising the step of disposing at least one electrode on the first substrate, the second substrate, or the first substrate and the second substrate.

29. The process of claim 28, wherein the at least one electrode is sandwiched between two material layers of the plurality of material layers.

30. The process of claim 16, further comprising the step of disposing at least one enhancement material in, or proximate to, each socket.

31. The process of claim 30, wherein the at least on enhancement material is selected from a group consisting of transistors, integrated-circuits, semiconductor devices, inductors, capacitors, resistors, control electronics, drive electronics, diodes, pulse-forming networks, pulse compressors, pulse transformers, and tuned-circuits.

32. The process of claim 31, wherein the step of disposing the at least one enhancement material in or proximate to each socket comprises the steps of:

3	suspending the at least one enhancement material in liquid; and		
4	flowing the liquid over the first substrate such that the at least one enhancement material		
5	settles in each	n socket.	
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1	33.	The process of claim 31, wherein the sockets are of a corresponding shape to the	
2	at least one en	nhancement material and wherein the at least one enhancement material self-aligns	
3	in each socke	t.	
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1	34.	The process of claim 16, further comprising the step of disposing a plurality of	
2	control electr	onics or drive electronics on the first substrate, the second substrate, or the first	
3	substrate and	the second substrate.	
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1	35.	The process of claim 16, wherein the web fabrication process is performed as a	
2	continuous hi	gh-speed inline process.	